

Name. _____		Printed Pages:01		
Student Admn. No.: _____				
School of Computing Science & Engineering Backlog Examination, June 2023 [Programme: B.Tech] [Semester: VI] [Batch: Backlog]				
Course Title: MICROPROCESSOR & INTERFACING		Max Marks: 100		
Course Code: CSE220		Time: 3 Hrs.		
Instructions:	1. All questions are compulsory. 2. Assume missing data suitably, if any.			
		K Level	COs	Marks
SECTION-A (15 Marks)		5 Marks each		
1.	Write any six features of Intel 8086 microprocessor.	K1	CO1	5
2.	What is the function of IN and OUT instruction?	K2	CO1	5
3.	Which group of instructions will affect the flags?	K1	CO2	5
SECTION-B (40 Marks)		10 Marks each		
4.	What is the difference between AND & TEST instruction?	K1	CO2	10
5.	Write a program to convert 53 into 35.	K3	CO2	10
6.	Explain the MOV instruction coding format of 8086.	K4	CO2	10
7.	Give an example of Register addressing mode. OR Give an example of Immediate addressing mode.	K3	CO2	10
SECTION-C (45 Marks)		15 Marks each		
8.	Write an assembly language program to find one's complement and two's complement of an 8-bit number.	K4	CO2	15
9.	Draw a diagram showing the bit format used for asynchronous Serial data.	K6	CO3	15
10	Explain the priorities of 8086 interrupts. OR How DMA can be interfaced with 8086? Explain with the help of a diagram.	K5	CO4	15