Course Code : BSCP3005

Course Name: Digital System and Application

Boolean Algebra and De Morgan's Theorems

Contents

- Introduction
- Duality Principle
- Boolean expressions
- Different Boolean Laws
- De Morgan's Theorems

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Introduction : Boolean Algebra

- George Boole developed an algebraic description for processes involving logical thought and reasoning.
 - Became known as <u>Boolean Algebra</u>
- Claude Shannon later demonstrated that Boolean Algebra could be used to describe switching circuits.
 - Switching circuits are circuits built from devices that switch between two states (e.g. 0 and 1).
 - <u>Switching Algebra</u> is a special case of Boolean Algebra in which all variables take on just two distinct values
- Boolean Algebra is a powerful tool for analyzing and designing logic circuits.

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Boolean Expressions

- Boolean expressions are composed of
 - 1- Literals variables and their complements
 - 2- Logical operations
 - Examples

literals

logic operations

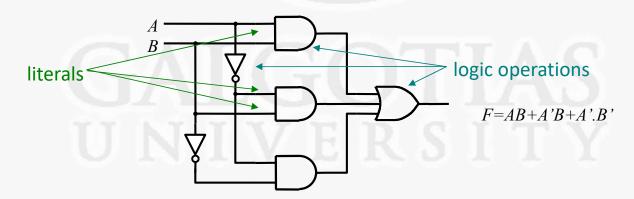
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Boolean Expressions

- Boolean expressions are realized using a network (or combination) of logic gates.
 - Each logic gate implements one of the logic operations in the Boolean expression
 - Each input to a logic gate represents one of the literals in the Boolean expression



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Duality Principle:

The duality principle states that a Boolean expression remains valid if operators OR and AND are interchanged and 1's and 0's in the expression are also interchanged.

In order to understand this principle, consider the Boolean Theorem 1 viz.

 $\mathsf{A} + \mathsf{O} = \mathsf{A}$

According to duality principle, this Boolean expression remains valid if OR function is replaced

by AND function and 0 by 1. In that case, the Boolean expression becomes :

 $\mathsf{A}\cdot\mathsf{1}=\mathsf{A}$

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Basic Laws and Theorems

Commutative Law	A + B = B + A	A.B = B.A
Associative Law	A + (B + C) = (A + B) + C	A . (<mark>B . C) = (</mark> A . B) . C
Distributive Law	A.(<mark>B + C</mark>) = AB + AC	A + (<mark>B . C) = (</mark> A + B) . (A + C)
Null Elements	A + 1 = 1	$A \cdot 0 = 0$
Identity	A + 0 = A	A . 1 = A
Idempotence	A + A = A	$A \cdot A = A$
Complement	A + A' = 1	A . A' = 0
Involution	A" = A	
Absorption (Covering)	A + AB = A	A . (A + B) = A
Simplification	A + A'B = A + B	A . (A' + B) = A . B
DeMorgan's Rule	(A + B)' = A'.B'	(A . B)' = A' + B'
Logic Adjacency (Combining)	AB + AB' = A	(A + B) . (A + B') = A
Consensus	AB + BC + A'C = AB + A'C	(A + B) . (B + C) . (A' + C) = (A + B) . (A' + C)

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Idempotence

A + A = A

F = ABC + ABC' + ABC

F = ABC + ABC'

Note: terms can also be added using this theorem

 $A \cdot A = A$

G = (A' + B + C').(A + B' + C).(A + B' + C)G = (A' + B + C') + (A + B' + C)

Note: terms can also be added using this theorem

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Complement

A + A' = 1F = ABC'D + ABCD F = ABD.(C' + C) F = ABD

$$A \cdot A' = 0$$

 $G = (A + B + C + D) \cdot (A + B' + C + D)$
 $G = (A + C + D) + (B \cdot B')$
 $G = A + C + D$

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Distributive Law

- A.(B + C) = AB + AC
 - F = WX.(Y + Z)
 - F = WXY + WXZ

- A + (B.C) = (A + B).(A + C)
 - F = WX + (Y.Z)
 - F = (WX + Y).(WX + Z)

- G = B'.(AC + AD)
- G = AB'C + AB'D

- G = B' + (A.C.D)
- G = (B' + A).(B' + C).(B' + D)

- H = A.(W'X + WX' + YZ)
- H = AW'X + AWX' + AYZ

- H = A + ((W'X).(WX'))
- H = (A + W'X).(A + WX')

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Absorption (Covering)

• A + AB = A

• A.(A + B) = A

- F = A'.(A' + BC) • F = A'BC + A'• F = A' • F = A'

- G = XY7 + XY'7 + X'Y'7' + X7
 - G = XY7 + X7 + X'Y'7'
 - G = X7 + X'Y'7'

• H = D + DE + DEF• H = D

- G = XZ.(XZ + Y + Y')
 - G = XZ.(XZ + Y)
 - G = XZ
- H = D.(D + E + EF)• H = D

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Simplification

A + A'B = A + B

F = (XY + Z).(Y'W + Z'V') + (XY + Z)'F = Y'W + Z'V' + (XY + Z)'

$$A.(A' + B) = A . B$$

 $G = (X + Y).((X + Y)' + (WZ))$
 $G = (X + Y) . WZ$

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Logic Adjacency (Combining)

A.B + A.B' = A

F = (X + Y).(W'X'Z) + (X + Y).(W'X'Z)'F = (X + Y)

$$(A + B).(A + B') = A$$

 $G = (XY + X'Z').(XY + (X'Z')')$
 $G = XY$

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Course Code : BSCP3005 Course Name: Digital System and Application Summary Single variable theorems Theorem 1 : A + 0 = ATheorem 2: $A \cdot l = A$ Theorem 3: A + A = 1Theorem 4: $A \cdot A = 0$ Theorem 5: A + A = A $A \cdot A = A$ Theorem 6: Theorem 7: A + 1 = 1 $A \cdot 0 = 0$ Theorem 8: = Theorem 9: A = A

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 Summary

 Multivariable theorems: These theorems refer to the condition when more than one input to the logic gate are variable.

LUDIC MUM			
Theorem 10 :	$ \left. \begin{array}{c} A + B = B + \dot{A} \\ A \cdot B = B \cdot A \end{array} \right\} Commutative Law $		
Theorem 11 :	$A \cdot B = B \cdot A \int Commutative Law$		
Theorem 12 :	$ A + (B + C) = (A + B) + C A \cdot (B \cdot C) = (A \cdot B) \cdot C $ Associative Law		
Theorem 13 :	$A \cdot (B \cdot C) = (A \cdot B) \cdot C \qquad \int Associative Law$		
Theorem 14 :	$A \cdot (B+C) = A \cdot B + A \cdot C$		
Theorem 15 :	$(A+B) \cdot (C+D) = A \cdot C + B \cdot C + A \cdot D + B \cdot D $ Distributive Law		
Theorem 16 :	$A + A \cdot B = A$		
Theorem 17 :	$(\overline{A+B}) = \overline{A} \cdot \overline{B}$ De Morgan's Theorems	0.1	
Theorem 18 :	$(\overline{A \cdot B}) = \overline{A} + \overline{B}$	5.1	



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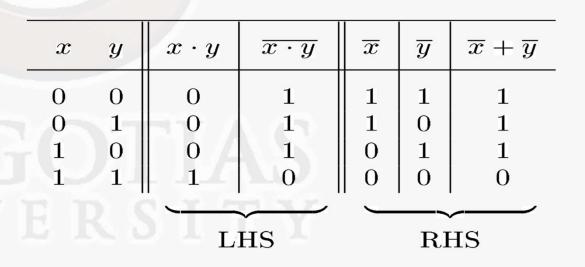
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De Morgan's Theorems

- Can be stated as follows:
 - The complement of the product (AND) is the sum (OR) of the complements.
 - (X.Y)' = X' + Y'
 - The complement of the sum (OR) is the product (AND) of the complements.
 - (X + Y)' = X' . Y'
- Easily generalized to n variables.
- Can be proven using a Truth table

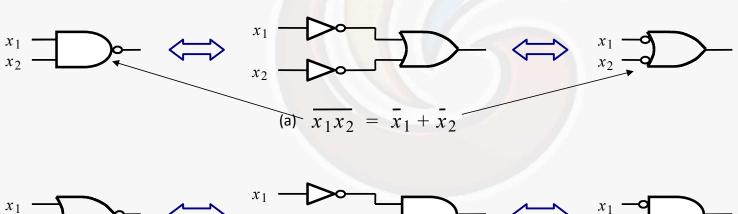
Proving De Morgan's Law

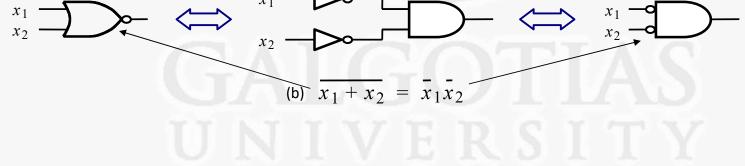


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De Morgan's Theorems





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Example:

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Simplify the expression:
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 $X = \overline{A}\overline{B}C + A\overline{B}C + AB\overline{C} + ABC$

Solution:

Step 1. Note that the first two terms have $\overline{B}C$ as common factors while the last two terms have *AB* as common factors.

$$\therefore \qquad X = \overline{BC} (A + \overline{A}) + AB (C + \overline{C})$$
Step 2. $A + \overline{A} = 1$ and $C + \overline{C} = 1$ so that :
 $X = \overline{BC} \cdot 1 + AB \cdot 1$
Step 3. Since $\overline{BC} \cdot 1 = \overline{BC}$ and $AB \cdot 1 = AB$ so that :
 $X = AB + \overline{BC}$

Note that not only is the Boolean expression simplified, but so is the resultant logic circuit.

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